

Isolation, fixed voltage input, regulated single output, SMD package

FEATURES

- International standard SMD packaging
- Isolation voltage 1500 VDC
- Sustainable short-circuit protection
- Low no-load input current
- High conversion efficiency
- Working temperature range:- 40~+85°C



D1-IB_XT series is an international standard straight-through SMD package, primarily used in pure digital circuits, low-frequency analog circuits, relay drive circuits, data exchange circuits, etc

SELECTION TABLE

Model	Input voltage(VDC)		Output voltage (VDC)	Output current		Efficiency (%)	Capacitive load (uF)
	Typ	Range		Min (mA)	Max(mA)		
D1-IB0503XT			3.3	30	303	67	2400
D1-IB0505XT			5	20	200	70	2400
D1-IB0509XT	5	4.75~5.25	9	12	111	71	1000
D1-IB0512XT			12	9	84	72	560
D1-IB0515XT			15	7	67	73	560
D1-IB1205XT			5	20	200	73	2400
D1-IB1212XT	12	11.4~12.6	12	9	84	73	560
D1-IB1215XT			15	7	67	74	560
D1-IB1503XT	15	14.25~15.75	5	20	200	73	2400
D1-IB2405XT			5	20	200	73	2400
D1-IB2412XT	24	22.8~25.2	12	9	84	73	560
D1-IB2415XT			15	7	67	73	560

INPUT

Parameter	Conditions/description	Min	Typ	Max	Units	
Input Current (Full Load/No Load)	5VDC input	3.3Vdc Output	/	300/8	325/--	
		12Vdc Output	/	290/8	308/--	
		9/12/15Vdc Output	/	285/8	305/--	
	12VDC input	3.3Vdc Output	/	120/8	128/--	mA
		12/15Vdc Output	/	115/8	124/--	
		15VDC input	/	99/8	105/--	
	24VDC input	5Vdc Output	/	60/4	66/--	
		12/15Vdc Output	/	59/4	64/--	
Refracted ripple current		/	15	/		
Input filter	capacitance filter					
Hot Plug	Unavailable					

OUTPUT

Parameter	Conditions/description	Min	Typ	Max	Units
Voltage accuracy	10% load to 100% load change	/	/	±3	
line regulation	Full load, input voltage variation ±1%	/	/	±0.25	%
load regulation	10% to100% load change	3.3V output	/	±3	
		Other output	/	±2	
Rippleand noise ¹	20MHz bandwidth (peak to peak)	/	70	/	mVp-p
Temperature coeffecient	100% load	/	±0.02	/	%/°C
Short circuit protection	Continuous, self-recovery				

Notes: 1. ripple and noise are measured at 20 MHz BW by "parallel cable" method with 1 µF ceramic and 10 µF electrolytic capacitors on the output.

COMPREHENSIVE

Parameter	Conditions/description	Min	Typ	Max	Units
Isolation voltage	input to output for 1 minute at 1 mA max.	1500	/	/	VDC
Isolation resistance	Input to output, insulation voltage 500 VDC	1000	/	/	MΩ
Isolation capacitor	Input-Output, 100 kHz/0.1 V	/	20	/	pF
Operating temperature	Use at reduced temperature ≥ 85°C. See the derating curve in Figure 1.	-40	/	85	
Storage temperature		-55	/	125	°C
Working temperature rise	at full load, Ta=25°C	/	25	/	
Welding Temperature	Manual-welding, Operation time 3-5 seconds	/	/	300	
	Wave soldering, Operation time 5-10 seconds	/	/	260	
Storage Humidity	non-condensing	/	/	95	%
Switching frequency	Nominal input voltage	/	240	/	kHZ
MTBF	MIL-HDBK-217F @ 25°C	3500	/	/	Khours
Cooling method	Natural air cooling				
Dimensions	15.24 x 11.40 x 7.25mm (0.600 x 0.449 x 0.285 inch)				
Weight	1.2g(Typ.)				
Case material	Black plastic; flame-retardant and heat-resistant plastic (UL94-V0)				

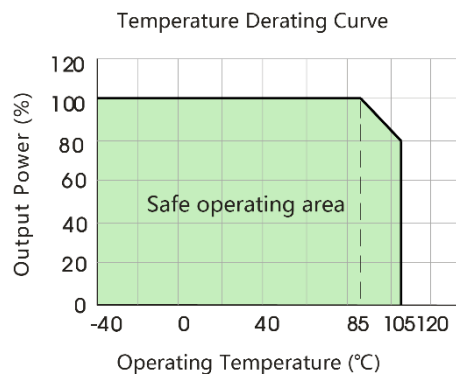
EMC

Parameter	Conditions/description
Emissions	CE CISPR32/EN55032 CLASS B (see Fig. 2 for recommended circuit)
	RE CISPR32/EN55032 CLASS B (see Fig. 2 for recommended circuit)
Immunity	ESD EC/EN61000-4-2 Air ±8kV, Contact ±6kV perf. Criteria B

Note: For EMC characteristic test circuits, see (Figure 4).

Product characteristic curve

(Figure 1) Temperature curve



Design reference

1. General typical application circuits (as shown in the following figure)

If further reduction of input and output ripple is required, a filtering network can be connected at the input and output terminals, as shown in Figure 2. Choose a suitable filtering capacitor. If the capacitor is too large, it may cause startup problems. Under the condition of ensuring safe and reliable operation, the reference capacitance value is recommended in the table on the right. For applications with actual output power less than 0.5W, it is recommended not to connect external capacitors.

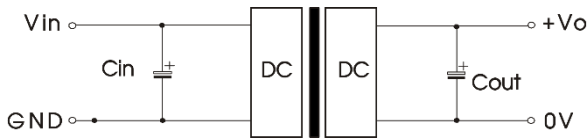


Figure 1

Input voltage (Vdc)	Capacitance Cin	Output voltage (Vdc)	Capacitance Cout
5	4.7uF/16V	3.3/5	10uF/16V
12	2.2uF/16V	12/15	2.2uF/25V
15	1uF/25V	15	1uF/25V
24	1uF/50V	15	0.47uF/25V

Table 1

2. EMC Recommended Application Circuits (For parameter details, see Table 2)

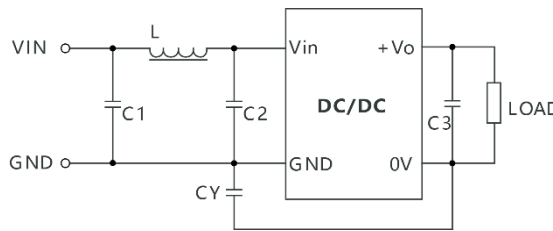
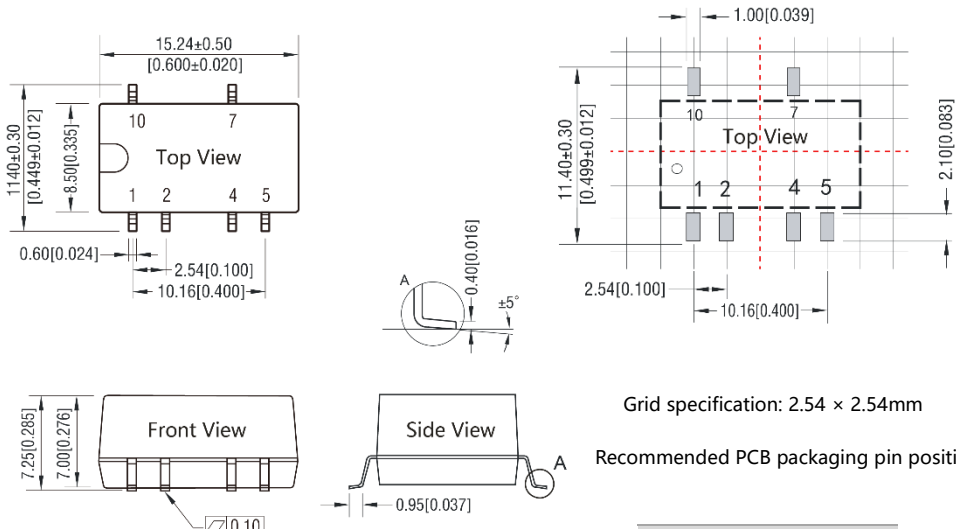


Figure 2

Input voltage (VDC)	5	Others	
Output voltage (VDC)	3.3/5/9	12/15	5/12/15
C1/C2	4.7uF/25V	4.7uF/25V	4.7uF/50V
CY	47pF/2kVdc	1nF/2kVdc	270pF/2kVdc
C3	Refer to Table 1 for Cout parameters		
L	6.8uH		

Table 2

MECHANICAL DRAWING



Grid specification: 2.54 × 2.54mm

Recommended PCB packaging pin positions (top view)

PIN CONNECTIONS	
1	GND
2	Vin
4	0V
5	+Vo
3,6,7,8	NC

NC: Does not connect to any external circuits

units: mm[inch]
tolerance: ±0.25[±0.010]
pin section tolerance: ±0.10[±0.004]

- Note:
1. Qituo technology reserves the right to change the product at any time without notice;
 2. The product shall be provided with a 3-year warranty period;
 3. Unless otherwise specified, the products in this manual are not authorized to be used for key components of equipment requiring high reliability, so as not to affect the safety or effectiveness of the device;
 4. All parameters in this manual are measured under indoor $t_a=25\text{ }^\circ\text{C}$, humidity <75%, nominal input voltage and output rated load;